Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1-2. (Canceled)

3. (Previously Presented) The optical interconnection circuit according to claim 16,

at least a part of the first optical waveguide and the second optical waveguide being provided on top surfaces of the first circuit block and the second circuit block.

4. (Previously Presented) The optical interconnection circuit according to claim 16,

at least a part of the first optical waveguide being provided on the first circuit block and the second circuit block to traverse the first circuit block and the second circuit block.

5. (Previously Presented) The optical interconnection circuit according to claim 16,

at least a part of the first optical waveguide being provided to detour around a third circuit block.

6. (Previously Presented) The optical interconnection circuit according to claim 16,

the first and the second elements being electrically connected to the first circuit block, and

the third and the fourth elements being electrically connected to the second circuit block.

7. (Canceled)

8. (Previously Presented) The optical interconnection circuit according to claim 16,

at least a part of the first optical waveguide covering at least the first element and the third element, and

at least part of the second optical waveguide covering at least part of the second element and the fourth element.

9. (Previously Presented) The optical interconnection circuit according to claim 16,

the first circuit block and the second circuit block being any one of a CPU, a memory circuit, a DSP, an RF amplifying circuit, an image sensor, and a bio sensor, and

the first optical waveguide and the second optical waveguide being a transmission line of data signals or clock signals.

10-11. (Canceled)

12. (Previously Presented) The optical interconnection circuit according to claim 16,

a plurality of the integrated circuit chips being mounted on a substrate, and

the plurality of integrated circuit chips being optically connected to each other at least through the first element and the third element and the first optical waveguide provided on the substrate.

13. (Previously Presented) The optical interconnection circuit according to claim 16,

a plurality of the integrated circuit chips being mounted on a substrate,

the integrated circuit chips being tightly bonded to each other, and

the integrated circuit chips being optically or electrically connected to each other.

14. (Previously Presented) An electro-optical device, comprising:

the optical interconnection circuit according to claim 16.

15. (Previously Presented) An electronic apparatus, comprising:

the optical interconnection circuit according to claim 16.

16. (Currently Amended) An optical interconnection circuit, comprising:

an integrated circuit chip;

a first circuit block provided on the integrated circuit chip, the first circuit block including a first element emitting a first light and a second element emitting a second light;

a second circuit block provided on the integrated circuit chip, the second circuit block including a third element receiving the first light and a fourth element receiving a second light;

a first optical waveguide that is provided on the integrated circuit chip, the first optical waveguide optically connecting the first and the third elements;

a second optical waveguide that is provided on the integrated circuit chip, the second optical waveguide optically connecting the second and the fourth elements;

a common light reflecting frame that reflects the first light and the second light; and

a wavelength of the first light emitted by the first element being different from a wavelength of the second light emitted by the second element,

the first circuit block and the second circuit block being optically and electrically connected to each other.

17-23. (Canceled)

24. (Previously Presented) The optical interconnection circuit according to claim 16,

the common light reflecting frame reflecting the first light and the second light so that the first light and the second light enters the first optical waveguide and the second optical waveguide, respectively.

25. (New) An optical interconnection circuit, comprising:

an integrated circuit chip;

a first circuit block provided on the integrated circuit chip, the first circuit block including a first element emitting a first light and a second element emitting a second light;

a second circuit block provided on the integrated circuit chip, the second circuit block including a third element receiving the first light and a fourth element receiving a second light;

a first optical waveguide that is provided on the integrated circuit chip, the first optical waveguide optically connecting the first and the third elements;

a second optical waveguide that is provided on the integrated circuit chip, the second optical waveguide optically connecting the second and the fourth elements;

a common light reflecting frame that reflects the first light and the second light; and

a wavelength of the first light emitted by the first element being different from a wavelength of the second light emitted by the second element, wherein

at least a part of the first optical waveguide is provided to detour around a third circuit block.